### Multilayer Ceramic Chip Capacitors

# C1608X7R1V105M080AC







1 of 3 Creation Date : May 08, 2017 (GMT)



#### **TDK item description** C1608X7R1V105MT\*\*\*\*

Applications	Commercial Grade	
Feature	General General (Up to 50V)	
Series	C1608 [EIA 0603]	
Status	Production (Not Recommended for New Design)	



Size		
Length(L)	1.60mm ±0.10mm	
Width(W)	0.80mm ±0.10mm	
Thickness(T)	0.80mm ±0.10mm	
Terminal Width(B)	0.20mm Min.	
Terminal Spacing(G)	0.30mm Min.	
Recommended Land Pattern (PA)	0.70mm to 1.00mm(Flow Soldering)	
Recommended Land Fattern (FA)	0.60mm to 0.80mm(Reflow Soldering)	
Recommended Land Pattern (PB)	0.80mm to 1.00mm(Flow Soldering)	
Recommended Land Fattern (FB)	0.60mm to 0.80mm(Reflow Soldering)	
Recommended Land Pattern (PC)	0.60mm to 0.80mm(Flow Soldering)	
Neconimenaea Lana Fattern (FC)	0.60mm to 0.80mm(Reflow Soldering)	

Electrical Characteristics		
Capacitance	1µF ±20%	
Rated Voltage	35VDC	
Temperature Characteristic	X7R(±15%)	
Dissipation Factor (Max.)	7.5%	
Insulation Resistance (Min.)	500ΜΩ	

Other	
Coldaring Mathod	Wave (Flow)
Soldering Method	Reflow
AEC-Q200	No
Packing	Punched (Paper)Taping [180mm Reel]
Package Quantity	4000pcs

<sup>!</sup> Images are for reference only and show exemplary products. ! This PDF document was created based on the data listed on the TDK Corporation website.

<sup>!</sup> All specifications are subject to change without notice.

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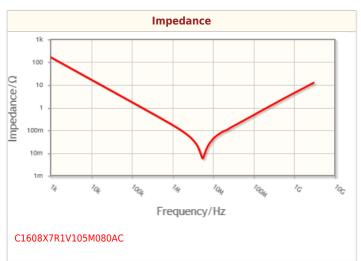


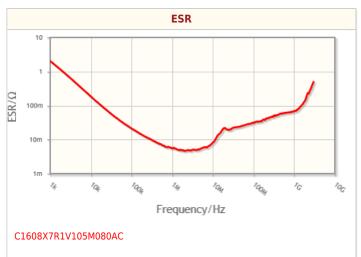


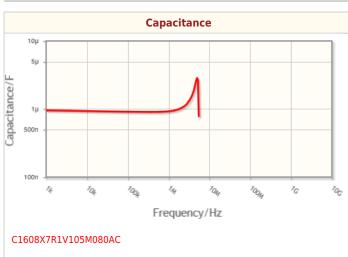


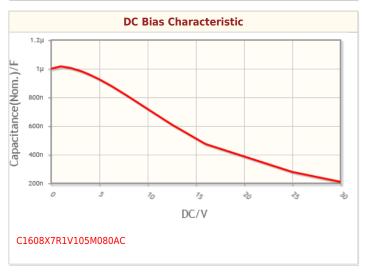


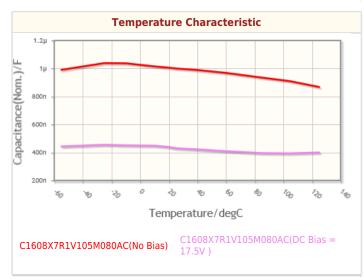
## Characteristic Graphs(This is reference data, and does not guarantee the products characteristics.)

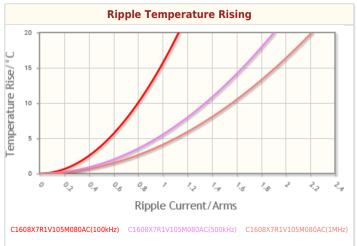












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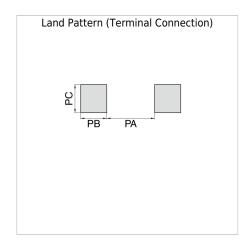








# **Associated Images**



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